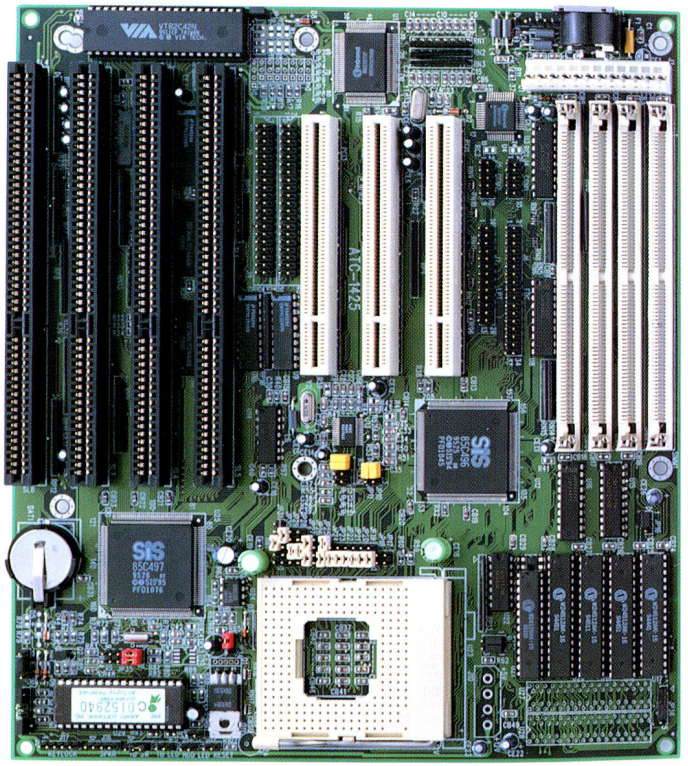
1425BR2

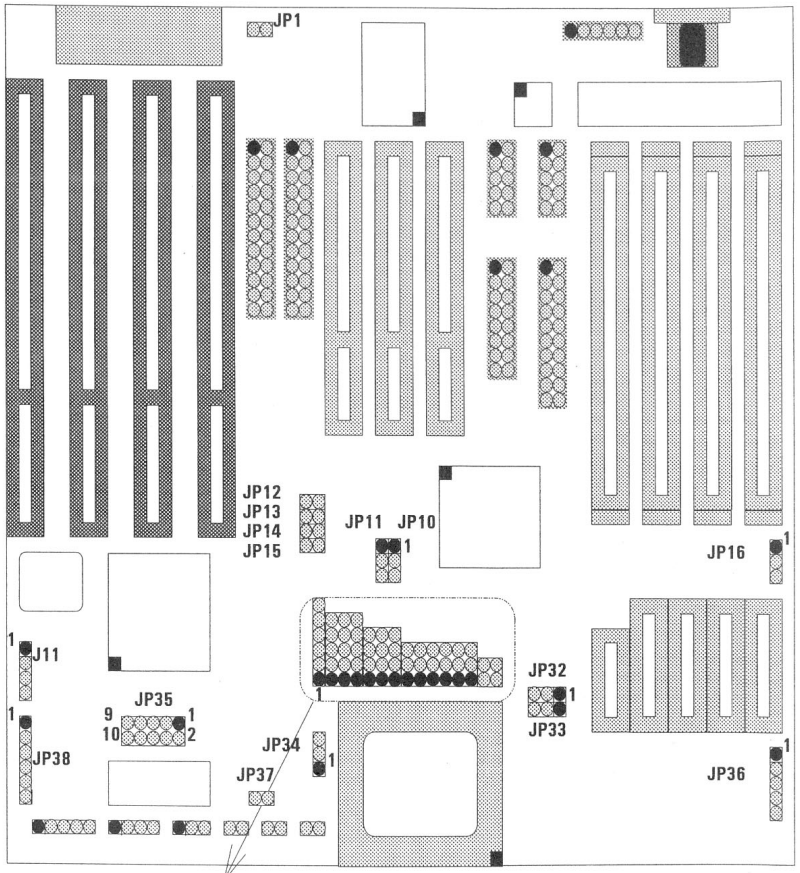
SiS 496/497

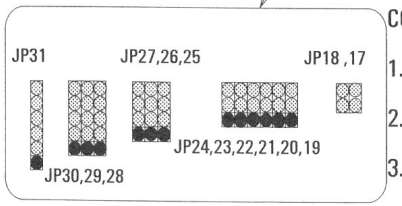
486 PCI Mainboard

user's manual



JUMPER LOCATION DIAGRAM OF SIS 486 PCI/ISA M/B

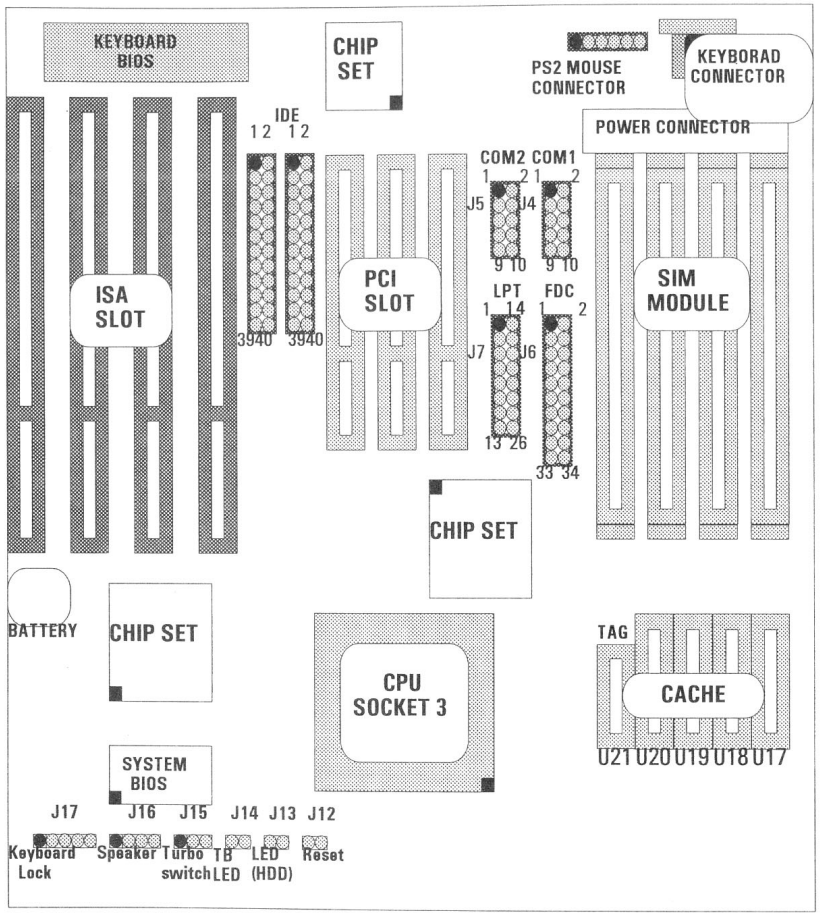




COLOR JUMPER CAPS CLASSIFICATION:

1. White----------CPU Type Selection.
2. Yellow----------System Clock Selection.
3. Red----------Regulator Voltage Selection.
4. Black----------Factory Setting.

CONNECTOR LOCATION DIAGRAM



CHAPTER 1 INTRODUCTION

1-1 SYSTEM FEATURES

- Supports all of 5V and 3.3V 486 CPUs, includes AmDX4-100/120, Am5x86-P75, and Cyrix 5x86.
- Flexible architecture to support 128/256/512KB Direct mapped cache scheme.
- Supports 4 stage power saving: On/Doze/Standby/Suspend.
- Supports L1/L2 Write back/Write through cache feature.
- Supports 72-pin dual type SIM modules.
- Memory configurations from 1MB up to 256M by using 256K/ 1M/4M/8M/16M/32M/64MB SIM module ,EDO DRAM function also supported.
- Supports SMI/SMM/PMU/APM power controllers.
- Dual IDE connectors support up to four devices in two channels.
- On-board high speed I/O includes 2 serial (16550 fast UART compatible), 1 parallel (EPP/ECP), and 1 FDC.

REMARKS

1-2 CHECK LIST OF THE PACKING

The mainboard comes securely packed in a durable box and shipping carton. Inside the box, the mainboard is sandwiched between two sheets of sponge and packed in an anti-static bag.

Each mainboard containing:

Q'TY	Description
1	Mainboard : ATC-1425B
1	Diskette : Enhanced IDE driver (3.5").
1	Cable : Enhanced IDE connector.
1	Cable : F.D.D connector.
1	Cable : Serial port.
1	Cable : Serial/Parallel.
1	Manual : User`s manual.

If any of the above items is missing or damaged, please contact the dealer from whom you purchased.

NOTE : Leave the mainboard in its original packing until you are ready to install it.

CHEAPTER 2 INSTALLATION

2-1 CPU INSTALLATION

The following table listed Jumpers related locations areshown in page 1.

2-1-1 CPU type setting

A. Gerneral setting

CPU type	JP18	JP19	JP20	JP21	JP22	JP23	JP24
486DX(5V)	open	open	1-2	2-3	2-3	open	open
486SX	open	open	1-2	2-3	2-3	open	open

CPU type	JP25	JP26	JP27	JP28	JP29	JP30	JP31
486DX(5V)	open	open	1-2,3-4	open	open	open	3-4
486SX	open	open	2-3	open	open	open	open

B. INTEL CPU setting

CPU type	JP18	JP19	JP20	JP21	JP22	JP23	JP24
P24T	open	1-2	1-2	open	1-2	open	1-2
P24D	open	1-2	1-2	1-2	1-2	1-2	1-2
S-SERIES	open	1-2	1-2	2-3	2-3	open	open

CPU type	JP25	JP26	JP27	JP28	JP29	JP30	JP31
P24T	3-4	1-2	1-2,3-4	2-3	open	1-2	2-3
P24D	1-2,3-4	open	1-2,3-4	2-3	2-3	4-5	3-4
S-SERIES	3-4	open	1-2,3-4	2-3	1-2	4-5	open

C. AMD CPU setting

CPU type	JP18	JP19	JP20	JP21	JP22	JP23	JP24
Am5x86-P75	close	1-2	1-2	1-2	1-2	1-2	1-2
Enhanced Am486	open	1-2	1-2	1-2	1-2	1-2	1-2
Am486DXL(3.45V)	open	2-3	※1-2/2-3	open	2-3	open	open

CPU type	JP25	JP26	JP27	JP28	JP29	JP30	JP31
Am5x86-P75	1-2,3-4	open	1-2,3-4	2-3	2-3	4-5	3-4
Enhanced Am486	1-2,3-4	open	1-2,3-4	2-3	2-3	4-5	3-4
Am486DXL(3.45V)	open	3-4	1-2,3-4	4-5	open	open	1-2,3-4

(※1-2 486DX4; 2-3 486DX2)

D. CYRIX CPU setting

CPU type	JP18	JP19	JP20	JP21	JP22	JP23	JP24
CYRIX CX486	open	1-2	1-2	1-2	2-3	open	2-3
CYRIX 5x86	open	1-2	1-2	1-2	2-3	1-2	1-2

CPU type	JP25	JP26	JP27	JP28	JP29	JP30	JP31
CYRIX CX486	2-3	2-3	1-2,3-4	1-2	open	2-3	3-4
CYRIX 5x86	1-2,3-4	open	1-2,3-4	2-3	2-3	4-5	3-4

2-1-2 CPU speed setting

CPU SPEED	JP14	JP13	JP12
DX25/DX2-50/DX4-75	OPEN	OPEN	OPEN
DX33/DX2-66/DX4-100	OPEN	CLOSE	CLOSE
DX40/DX2-80/DX4-120	OPEN	OPEN	CLOSE
DX50	OPEN	CLOSE	OPEN

2-1-3 CPU voltage setting

CPU Voltage	JP34	JP35
5V	1-2	open
3.45V	2-3	1-3,2-4
4V	2-3	3-5,4-6

2-2 SIM MODULES INSTALLATION

The SiS 486 PCI/ISA mainboard can be expanded from 1MB to 256MB by using 256K, 1MB, 2MB, 4MB, 8MB, 16MB, 32MB, and 64MB of 72-pin SIM module.

※ **"Free Table"** feature is offered for main memory configuration. The product works with one SIMM plugs into any SIMM sockets.

2-3 SRAM INSTALLATION

This mainboard can support cache memory from128K to 512K bytes with the types of 32KX8, 64KX8, or 128KX8 SRAM.

Cache Configuration Size

Cache Size	TAG SRAM (U21)	DATA SRAM (U17-20)	JP16	JP32	JP33	JP36
128K	**8KX8**	**32KX8**	1-2	1-2	1-2	1-2,3-4
256K	**32KX8**	**64KX8**	2-3	1-2	2-3	1-2,3-4
512K	**32KX8**	**128KX8**	1-2	2-3	2-3	1-2,3-4

2-4 OTHER JUMPERS SETTING

2-4-1 Other jumpers setting

	JP10
Synchronous clock	1-2
Asynchronous clock	2-3

	JP38※
Flash ROM	1-2, 5-6
EPROM	2-3, 5-6

※ The pin-5 and pin-6 is fixed by factory default setting.

	J11
External Battery	1-2
Internal Battery	2-3
Clear CMOS	3-4

2-4-2 Connectors description

Connector	Description
J12	Reset
J13	HDD LED
J14	Turbo LED
J15	Turbo Switch
J16	Speaker Connector
J17	Keylock